

Workshop on Software Verification and Runtime Analysis

https://vassal.fit.vut.cz/vassal-workshop-on-software-verification-and-runtime-analysis

September 15, 2025 in Graz, Austria

Call for Presentations

We invite presentation abstracts (**1-2 pages**) describing ongoing research, recent results, concrete use cases, or works already published in international venues.

At least one author of the accepted presentation is expected to present their work at the workshop at Graz, Austria.

Dates

Submission deadline: July 16, 2025

Notification: July 30, 2025

Workshop: September 15, 2025

Organisers

- Julien Signoles (Université Paris-Saclay, CEA, List)
- Milan Češka (Brno University of Technology)

Topics

We welcome submissions on all aspects of software verification, runtime analysis, and system safety, including but not limited to:

- Logic, automata, and decision procedures
- Formal specification languages and correctness proofs
- Runtime monitoring, enforcement, and dynamic analysis
- Automated verification, synthesis, and static analysis
- Integration of static verification with runtime monitoring
- Al-driven techniques for verification and system analysis
- Economic evaluation of verification tools in industry

Contributions addressing challenges in safety-critical systems (e.g., automotive, aerospace, medical devices) are especially encouraged.

Submissions

Presentation abstracts (1-2 pages) must be written in English and submitted electronically in PDF format using the link below. The 1-2 page limit includes all text and figures, but excludes references.

Submission link: https://easychair.org/conferences?conf=vassal2025